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### **Module 5: Theories Of Failure - VTU Updates | VTU Notes**

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Dr. C V Srinivasa, Department Of Civil Engineering Global Academy Of Technology,  
RR Nagar, Bengaluru-560098 Svasa@gat.ac.in, 94498 09918 Module 5: Theories Of  
Failure Objectives: The Objectives/outcomes Of This Lecture On “Theories Of  
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### **ECE Department University Of Arizona ECE 340 ...**

• S. Haykin, B. Van Veen, Signals And Systems, 2nd Ed., John Wiley & Sons, 2003.  
Office Hours • 2:00 PM – 3:00 PM, Tuesdays • 4:00 PM – 5:00 PM, Thursdays  
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O S. Kilts, "Advanced FPGA Design", (Wiley), ISBN 978-0-05437-6 O H. Bhatnagar, "Advanced ASIC Chip Synthesis Using Synopsys Design Compiler, Physical Compiler, And PrimeTime", ISBN 0-7923-7644-7 1th, 2024

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