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Weebly Microprocessor Trainer Kit. EQUIPMENT REQUIRED Intel 8085 Based MPS – 85 – 2 Trainer Kit, DAC Interface Card, 26 Pin Flat Cable Connector, CRO With Probe. ALGORITHM:- SQUARE WAVE FORM 1. Initialize The Control Word Register I.e., 8255 As Input Port. 2. Send 00 To Port A Of 8255 3. Call Delay

Subroutine 4. Send FF To Port A Of 8255 5. 1th,  
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Microprocessor (MPU) Is A Computing And Logic Device  
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MODEL : VMC-ICE8085 8085 CPU Operating At 6.144 MHz Crystal Frequency. Comprises Of A)

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ManualDIVISION OF TWO 16-BIT DATA AIM: To Multiply Two 16-bit Data (Multibyte Division) Using 8086 Microprocessor. APPARATUS: 1. 8086 Trainer Kit 2. Key Board 3. SMPS PROGRAM CODE: MOV AX,[0300] MOV BX,[0302] DIV BX INT A5 RESULT: MICROPROCESSORS & INTERFACING DEVICES Microprocessor And

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