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8086-1 Will Release The Local Bus During T4 Of The Cycle When All The Following Conditions Are Met:

1. Request Occurs On Or Before T2. 2. Current Cycle Is Not The Low Byte Of A Word (on An Odd Address).

3. Current Cycle Is Not The First Acknowledge Of An Interrupt Acknowledge Sequence. 4. A Locked

Instruction Is Not Currently Executing. 3th,

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(cont..)8086 Microprocessor (cont..) M. Krishna Kumar  
MM/M1/LU3/V1/2004 2 • It Requires Single Phase Clock  
With 33% Duty Cycle To Provide Internal Timing. •  
8086 Is Designed To Operate In Two Modes, Minimum  
And Maximum. • I 1th, 2024.  
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Peripherals And How An 8086 Based Microcomputer  
System Works. 3.2 Learning Objectives To Study About  
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A. Deshmukh, SKNCOE, Comp Memory Address Space  
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It Was The First 16-bit Microprocessor. 3. This  
Microprocessor Had Major Improvement Over The  
Execution Speed Of 8085. 4. It Is Available As 40-pin  
Dual-Inline-Package (DIP). 5. It Is Available In Three  
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