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Decimation Of A Factor M. In Practice It Reduces The Sampling Frequency Of A Factor M (downsampling). 1) Consider The Sequence $X[n] = 3^n$, 2024.

Discrete-Time Signal Processing (DSP) Digital Signal Processing (Kuhn 2005)

Analog/digital And Digital/analog Converters, CPU, DSP, ASIC, FPGA Advantages:

- ¾noise Is Easy To Control After Initial Quantization
- ¾highly Linear (with Limited Dynamic Range)
- ¾complex Algorithms Fit Into A Single Chip
- ¾flexibility, Parameters Can Be Varied In Software

1th, 2024 Discrete Time Signal Processing Computer (Figure 1.1(b)). These Represent A Continuous-time, Discrete-time And Digital Signal Respec-tively. As We Know From The Sampling Theorem, The Continuous-time Signal Can Be Reconstructed From Its Sam-ples Taken With A Sampling Rate At Least Twice The Highest Frequency Component In The Signal 1th, 2024

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