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B.Tech In Electronics And Communication Engineering, Cochin University Of Science And Technology, India 2000-2004. PROFESSIONAL EXPERIENCE . Micron. Boise, USA 2016-2018. Package Signal And Power Integrity Engineer, Participated In The DDR5 Package Electrical Specification Development, Seagate (formerly LSI / Avago Technologies) May 3th, 2024

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Power Delivery Analysis, And Characterization Of PCB, Package And On-chip Interconnects At Various Companies, Including Seagate (formerly Avago LSI Flash Storage Division) And Tektronix. He Received A BTech Degree In ECE From The Cochin University Of Science And Technology In 2004. He Has Written Several Papers And Received The Best Feb 2th, 2024

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Ramesh Et Al. World Journal Of Pharmaceutical Research ...

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Summary Name : Dr. Ramesh Kumar Pandey Date Of Birth ...

BIO DATA Name : Dr. Ramesh Kumar Pandey Father's Name: Late Shri. Girish Deo Pandey Date Of Birth : 02.08.1952 Permanent Address : "Pandey Niwas" Gandhi N Jun 2th, 2024

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