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2.5(RR) 2.5(R)(1arm&LHF) 1.5S No Arm 2.5S No Arm 3(RR) 3(R ...

DESIGN: (Kato)HO2B P1 Armchair 1S 1arm(LHF) 1.5S 1arm(LHF) 2(RR) Dual Lift Chair 2S No Arm 3S No Arm 3 3S 1arm(LHF)seater Settee 2 Seater Settee 2.5 Seater Settee 2.5S 1arm(LHF) 4th, 2024

1 Arm J1:1 - A518 (W) T Arm J1:2 - A518 (E) - Straight

Arm J1:1 - A518 (W) 1 Arm J1:2 - A518 (E) - Straight 1 P 1) 1 2 B B 1 T) 1 T 1 T 1 A B
C. Full Input Data And Results . Network Results . Item Lane Description Lane Type
Controller Stream Position In Filtered Route Full Phase 2th, 2024

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62415 Br43 223080 K8036 (2) 2.750 0.750 1.922 2.078 2.078 Or 62416 62418 Br52
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0 5 10 15 20 25 30 35 40 1 2 4 MIPS Number Of Cores (Instruction Set Simulators)
Avg. ISS MIPS Acc. System MIPS 3 VP Simulation Performance N Example: UC/MC VP
Performance (MIPS)-Target SW: COREMARK/Linux-Test Single/dual/quad Core
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